2. solder
3. component
4. outline
5. componentmask
6. soldermask
7. plated-drill
8. topsilk
9. bottomsilk
10. topassembly
11. bottomassembly
12. fab
ix_revC, back silk (mirrored), scale = 1:1.000
/Users/oskay/Documents/UnixSoftware/gedfiles/ix_RevC.pcb
There are 5 different drill sizes used in this layout, 89 holes total.

<table>
<thead>
<tr>
<th>Symbol</th>
<th>Dia. (Inch)</th>
<th>Count</th>
<th>Plotted?</th>
</tr>
</thead>
<tbody>
<tr>
<td>+</td>
<td>0.030</td>
<td>51</td>
<td>YES</td>
</tr>
<tr>
<td>+</td>
<td>0.030</td>
<td>32</td>
<td>YES</td>
</tr>
<tr>
<td>x</td>
<td>0.051</td>
<td>2</td>
<td>YES</td>
</tr>
<tr>
<td>x</td>
<td>0.060</td>
<td>2</td>
<td>YES</td>
</tr>
<tr>
<td>o</td>
<td>0.092</td>
<td>2</td>
<td>YES</td>
</tr>
</tbody>
</table>

Title: ix_revC - Fabrication Drawing
Author: Windell H. Oskay
Date: Wed Sep 30 11:27:08 2009 UTC

Board outline is the centerline of this path.